

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

Claim 1 (Currently Amended): A process for transmitting data between at least one transmitter [(1)] and at least one receiver [(2)], in the form of data packets of at least one datum, each of said data packets being associated with an identifier of said data packet,

[[characterised in that it]] wherein the process implements at least two transmission modes:

- an explicit mode [(12; 22)], wherein each of said data packets, called explicit packets, is transmitted with said identifier of said data packet;

- an implicit mode [(11; 21)], wherein said data packets, called implicit packets, are transmitted without being accompanied by said identifiers;

and [[in that it]] the process includes at least one first transfer stage [(24; 14)] from said explicit mode to said implicit mode and/or at least one second transfer stage [(23; 13)] from said implicit mode to said explicit mode, selection of one of the first and second transfer stages determined as a function of at least one pre-determined transfer criterion associated with the data packet.

Claim 2 (Currently Amended): A process according to claim 1, [[characterised in that]] wherein said receiver [(2)] maintains at least one of the [[variables belonging to the group]] following including:

- variables [(561, 551; 572, 552)] relating to said identifiers of said data packets;

- an error flag relating to said data transmission; and

- state variables relating to an implemented protocol.

Claim 3 (Currently Amended): A process according to claim 2, [[characterised in that]] wherein said error flag [[may take]] includes at least two states:

- a [[“]]raised[[”]] state after said receiver [(2)] receives an error message; and
- a [[“]]lowered[[”]] state after said receiver correctly receives an explicit packet.

Claim 4 (Currently Amended): A process according to claim 3, [[characterised in that]] wherein when said receiver [(2) being] is in the explicit mode [(22),] and said error flag [[being]] is raised, said receiver rejects all implicit packets received.

Claim 5 (Currently Amended): A process according to claim 3, [[characterised in that]] wherein when said receiver [(2) being] is in the explicit mode [(22)] and [[receiving]] receives at least one implicit packet[,] and said error flag [[being]] is raised, said receiver implements the following successive stages:

- said receiver stores, according to a sequential order of storage, said at least one received implicit packet;

- [[if]] upon said error flag [[remains]] remaining in the [[“]]raised[[”]] state, said receiver rejects said at least one [[stored]] implicit packet; and

- [[if]] upon said error flag [[passes]] passing to the [[“]]lowered[[”]] state, said receiver processes said at least one [[stored]] implicit packet and assigns to [[it the]] the at least one implicit packet an identifier which sequentially precedes said identifier of said received explicit packet, [[if it]] upon the at least one implicit packet is the first stored implicit packet, or upon the identifier which sequentially precedes the identifier of the

received explicit packet being an identifier for a previously stored implicit packet according to said sequential order of storage.

Claim 6 (Currently Amended): A process according to any one of claims 3 to 5, [[characterised in that]] wherein the transfer [[(24)]] of said receiver [[(2)]] from said explicit mode [[(22)]] to said implicit mode [[(21)]] is triggered by [[the]] receipt of an implicit packet, provided that said error flag is in the [[“]]lowered[[’]] state,

and [[in that]] said transfer from said implicit mode to said explicit mode [[(23)]] is triggered by the receipt of an explicit packet and/or an error message.

Claim 7 (Currently Amended): A process according to any one of claims 1 to [[6]] 5, [[characterised in that]] wherein said receiver [[(2)]] having correctly received a data packet, [[it]] said receiver sends to said transmitter [[(1)]] at least one conventional acknowledgement message of said received packet, containing said identifier of said next data packet expected by said receiver,

and [[in that,]] in at least some cases, said receiver, prior to said dispatch of said at least one conventional acknowledgement message, sends to said transmitter at least one advance acknowledgement message.

Claim 8 (Currently Amended): A process according to any one of claims 1 to [[7]] 5, [[characterised in that]] wherein said transmitter [[(1)]] maintains at least one of the [[variables belonging to the group]] following including:

- variables [[(51, 52)]] relating to identifiers of at least some of said transmitted packets;

- for each of said transmitted packets, a clock being able to take at least three states:

- an [“in progress”] in-progress state, after sending of said transmitted packet;
- a [“”]stopped[“”] state, after receipt of an acknowledgement message of said transmitted packet;
- an [“”]expired[“”] state, after a pre-determined maximum time; and
- state variables relating to an implemented protocol.

Claim 9 (Currently Amended): A process according to any one of claims 1 to [[8]] 5, [[characterised in that]] wherein the transfer [[(14)]] of said transmitter [[(1)]] from said explicit mode [[(12)]] to said implicit mode [[(11)]] is triggered by an event internal and/or external to said transmitter, if at least one explicit packet has been sent by said transmitter since the last transfer of said transmitter from said implicit mode to said explicit mode.

Claim 10 (Currently Amended): A process according to any one of claims 1 to [[9]] 5, [[characterised in that]] wherein said transmitter maintains additionally at least one of the variables belonging to the group including:

- a first identification variable [[(52), called]] EoW, whose value is an identifier of said next packet to be transmitted; and
- a second identification variable [[(51), called]] BoW, whose value is the smallest of said identifiers of said transmitted packets, for which said transmitter has not received an acknowledgement message, said identifier being a number, assigned sequentially to each of said data packets.

Claim 11 (Currently Amended): A process according to claim[[s 8 and 9, and possibly according to claim 10, characterised in that]] 8, wherein said identifier is a number, assigned sequentially to each of said data packets,

and [[in that]] said event is constituted by [[the]] a combination of the following conditions:

- said transmitter receives an acknowledgement message containing [[the]] an identifier N of the next data packet expected by said receiver; and
- said clock of each of said packets with identifiers SN transmitted by said transmitter is either in said [[“in progress”]] in-progress state, or in said [[“]]stopped[”]] state, SN being greater than or equal to N, and SN being strictly lower than [[the]] an identifier of said next packet to be transmitted (EoW).

Claim 12 (Currently Amended): A process according to claim 8, [[and possibly any one of claims 9 to 11, characterised in that]] wherein said identifier is a number, assigned sequentially to each of said data packets,

and [[in that]] the transfer [[(13)]] of said transmitter [[(1)]] from said implicit mode [[(11)]] to said explicit mode [[(12)]] is triggered by receipt of an acknowledgement message containing the identifier N of said next data packet expected by said receiver, if the two following conditions are confirmed:

- said transmitter has transmitted at least one packet with identifier SN, with SN greater than or equal to N, and SN being strictly lower than the identifier of said next packet to be transmitted; and
- said clock of one at least of said packets with identifier SN is in the [[“]]expired[”]] state.

Claim 13 (Currently Amended): A process according to claim[[s 8 and 10, and possibly according to claim 11, characterised in that]] 8, wherein said identifier is a number, assigned sequentially to each of said data packets,

and [[in that]] the transfer [[(13)]] of said transmitter [[(1)]] from said implicit mode [[(11)]] to said explicit mode [[(12)]] is triggered by the transfer of said clock of a packet with identifier SN into said [“]expired[”] state, SN being greater than or equal to said second identification variable BoW, and strictly lower than said first identification variable EoW.

Claim 14 (Currently Amended): A process according to any one of claims 1 to [[13]] 5, [[characterised in that]] wherein said explicit mode implements a protocol of the Automatic Repeat Request (ARQ) type.

Claim 15 (Currently Amended): A process according to claim 14, [[characterised in that]] wherein said ARQ type protocol belongs to the group including:

- protocols of the Go-back-N type;
- protocols of the Selective Repeat type; and
- protocols of the Stop-and-Wait type.

Claim 16 (Currently Amended): A system for transmitting data exchanged between at least one transmitter [[(1)]] and at least one receiver [[(2)]], in the form of packets of at least one datum, each of said data packets being associated with an identifier of said packet,

[[characterised in that it includes]] wherein the system is configured to include at least two transmission modes:

- an explicit mode [[(12; 22)]], wherein each of said data packets, called explicit packets, is transmitted with said identifier of said data packet;
- an implicit mode [[(11; 21)]], wherein said data packets, called implicit packets, are transmitted without being accompanied by said identifiers;

and [[in that it]]] the system implements first transfer means [[(24; 14)]] from said explicit mode to said implicit mode and/or second transfer means [[(23; 13)]] from said implicit mode to said explicit mode, selection of one of the first and second transfer means determined as a function of at least one pre-determined transfer criterion associated with the data packet.

Claim 17 (Currently Amended): A transmitter [[(1)]] of a data transmission system[[, of the type making it possible to exchange]] for exchanging data with at least one receiver [[(2)]], in the form of packets of at least one datum, each of said data packets being associated with an identifier of said packet,

[[characterised in that it operates]] wherein the transmitter is configured to operate according to at least two data transmission modes:

- an explicit mode [[(12)]], wherein each of said data packets, called explicit packets, is transmitted with said identifier of said data packet;

- an implicit mode [[(11)]], wherein said data packets, called implicit packets, are transmitted without being accompanied by said identifiers;

and in that it includes first transfer means [[(14)]] from said explicit mode to said implicit mode and/or second transfer means [[(13)]] from said implicit mode to said explicit mode, selection of one of the first and second transfer means determined as a function of at least one pre-determined transfer criterion associated with the data packet.

Claim 18 (Currently Amended): A receiver [[(2)]] of a data transmission system[[, of the type making it possible to exchange]] for exchanging data with at least one transmitter [[(1)]], in the form of packets of at least one datum, each of said data packets being associated with an identifier of said packet,

[[characterised in that it operates]] wherein the receiver is configured to operate
according to at least two data reception modes:

- an explicit mode [[(22)]], wherein each of said data packets, called explicit packets,
is transmitted with said identifier of said data packet;

- an implicit mode [[(21)]], wherein said data packets, called implicit packets, are
transmitted without being accompanied by said identifiers;

and [[in that it includes]] the receiver is configured to include first transfer means
[[(24)]] from said explicit mode to said implicit mode and/or second transfer means [[(23)]]
from said implicit mode to said explicit mode, selection of one of the first and second transfer
means determined as a function of at least one pre-determined transfer criterion associated
with the data packet.